

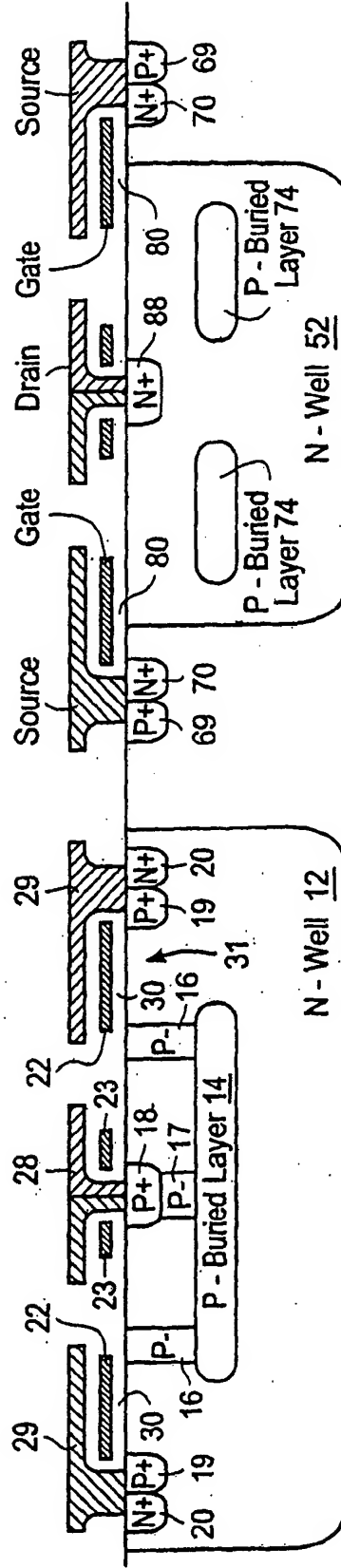
FIG. 1



FIG. 2

HV PMOS

HV NMOS



P-Substrate 10

FIG. 3